

Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Errors
1	BRS	L1	0	"10020568"	USPAT ; US-PG 15:49 PUB	2004/06/21		0
2	BRS	L2	2	714/45 & plurality & processor & unit & monitor ; US-PG 15:49 PUB	USPAT ; US-PG 15:49 PUB	2004/06/21		0
3	BRS	L3	15	709/230 & plurality & processor & unit & monitor ; US-PG 15:50 PUB	USPAT ; US-PG 15:50 PUB	2004/06/21		0
4	BRS	L4	268	709/230 & plurality & processor & unit & monitor & values & core & temperature & power & consumption & storage & trigger & state & cache	USPAT ; US-PG 15:51 PUB	2004/06/21		0
5	BRS	L5	67	709/230 & plurality & processor & unit & monitor & values & core & temperature & power & consumption & storage & trigger & state & cache	USPAT ; US-PG 15:51 PUB	2004/06/21		0
6	BRS	L6	1	702/182 & plurality & processor & unit & monitor ; US-PG 15:52 PUB	USPAT ; US-PG 15:52 PUB	2004/06/21		0
7	BRS	L7	0	702/183 & plurality & processor & unit & monitor ; US-PG 15:53 PUB	USPAT ; US-PG 15:53 PUB	2004/06/21		0
8	BRS	L8	0	702/186 & plurality & processor & unit & monitor ; US-PG 15:53 PUB	USPAT ; US-PG 15:53 PUB	2004/06/21		0

Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Errors
9 BRS	L9	0	702/188 & plurality & processor & unit & monitor & values & core & temperature & power & consumption & storage & trigger & state & cach3	SSPAT ; US-PG PUB	2004/06/21 15:54			0
10 BRS	L10	0	702/189 & plurality & processor & unit & monitor & values & core & temperature & power & consumption & storage & trigger & state & cach3	SSPAT ; US-PG PUB	2004/06/21 15:54			0